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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
KP	6,021,480	07-2002	Pettey	711	201	
KP	6,918,012	07-2005	Venkitakrishnan et al	711	150	
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FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

KP	Publication: "Specuculative Synchronization: Applying Thread-Level Speculation to Explicitly Parallel Applications" by Jose F. Martinez and Josep Torrellas, Dept. of Computer Science, University of Illinois at Urbana-Champaign, Urbana, IL 61801 USA, http://iacoma.cs.uiuc.edu, XP-002285169, published in ASPLOS X, 10-2002, Pgs. 18-29
KP	Publication: "Transactional Memory: Architectural Support for Lock-Free Data Structures" by Maurice Herlihy, Digital Equip. Corp. Cambridge Research Laboratory, Cambridge, MA 02139, herlihy@crl.dec.com and J. Eliot B. Moss, Dept. of Computer Science, University of Massachusetts, Amherst, MA 01003, moss@cs.umass.edu, XP-000380375, published in Computer Architecture News, May 21, 1993, Pgs. 289-300
KP	Publication: "Enhancing Software Reliability with Speculative Threads" by Jeffrey Oplinger and Monica S. Lam, Computer Systems, Laboratory, Stanford University, jeffop@stanford.edu, XP-002285168 published in SPLOS X, 10-2202, Pgs. 184-196
KP	Publication: "Transactional Memory: Architectural Support for Lock-Free Data Structures" by Maurice Herlihy, Digital Equip. Corp. Cambridge Research Laboratory, Cambridge, MA 02139, herlihy@crl.dec.com and J. Eliot B. Moss, Dept. of Computer Science, University of Massachusetts, Amherst, MA 01003, moss@cs.umass.edu, XP-000380375, published in Computer Architecture News, May 21, 1993, Pgs. 289-300

EXAMINER	DATE CONSIDERED
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